

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S84	25	S55 and S83	US-PGPU B; USPAT; EPO; JPO; DERWENT	OR	ON	2006/02/10 13:59
S83	40	(synchroni\$3 or sync) near3 (module or register) and antenna near array	US-PGPU B; USPAT; EPO; JPO; DERWENT	OR	ON	2006/02/10 13:59
S82	0	(synchroni\$3 or sync) adj (module or register) with antenna near array	US-PGPU B; USPAT; EPO; JPO; DERWENT	OR	ON	2006/02/10 13:59
S81	0	(synchroni\$3 or sync) adj register near2 receiv\$3 adj (signal or data) near5 antenna near array and beam adj former	US-PGPU B; USPAT; EPO; JPO; DERWENT	OR	ON	2006/02/10 13:58
S80	0	(synchroni\$3 or sync) adj register near2 receiv\$3 adj (signal or data) near5 antenna adj array and beam adj former	US-PGPU B; USPAT; EPO; JPO; DERWENT	OR	ON	2006/02/10 13:58
S79	1819887	(synchroni\$3 or sync) adj register near\$2 receiv\$3 adj (signal or data) near5 antenna adj array and beam adj former	US-PGPU B; USPAT; EPO; JPO; DERWENT	OR	ON	2006/02/10 13:57
S76	1819934	(synchroni\$3 or sync) adj register near\$2 receiv\$3 adj (signal or data) near5 antenna near array and beam adj former	US-PGPU B; USPAT; EPO; JPO; DERWENT	OR	ON	2006/02/10 13:57
S78	64	S55 and S77	US-PGPU B; USPAT; EPO; JPO; DERWENT	OR	ON	2006/02/10 13:56

S77	130	S61 and S76	US-PGPU B; USPAT; EPO; JPO; DERWEN T	OR	ON	2006/02/10 13:56
S61	415	(analog near2 digital near2 (convertor or converter) or DAC or ADC) near4 (adjust or delay) near2 tim\$3	US-PGPU B; USPAT; EPO; JPO; DERWEN T	OR	ON	2006/02/10 13:56
S74	1819744	synchroni\$3 adj module near\$2 receiv\$3 adj (signal or data) near5 antenna near array and beam adj former	US-PGPU B; USPAT; EPO; JPO; DERWEN T	OR	ON	2006/02/10 13:54
S75	1177553	S74 and S55	US-PGPU B; USPAT; EPO; JPO; DERWEN T	OR	ON	2006/02/10 13:53
S72	1819918	synchroni\$3 adj module near\$2 receiv\$3 with antenna near array and beam adj former	US-PGPU B; USPAT; EPO; JPO; DERWEN T	OR	ON	2006/02/10 13:52
S73	1177625	S55 and S72	US-PGPU B; USPAT; EPO; JPO; DERWEN T	OR	ON	2006/02/10 13:51
S71	1180227	S55 and S70	US-PGPU B; USPAT; EPO; JPO; DERWEN T	OR	ON	2006/02/10 13:50
S70	1825385	synchroni\$5 adj module near\$2 receiv\$3 with antenna near array	US-PGPU B; USPAT; EPO; JPO; DERWEN T	OR	ON	2006/02/10 13:50
S69	2216021	synchroni\$5 near\$2 receiv\$3 with antenna near array	US-PGPU B; USPAT; EPO; JPO; DERWEN T	OR	ON	2006/02/10 13:49

S68	2217688	synchroni\$5 near\$2 receiv\$3 same antenna near array	US-PGPU B; USPAT; EPO; JPO; DERWEN T	OR	ON	2006/02/10 13:49
S65	4553843	synchroni\$5 near\$3 group	US-PGPU B; USPAT; EPO; JPO; DERWEN T	OR	ON	2006/02/10 13:48
S67	5029	S55 and S66	US-PGPU B; USPAT; EPO; JPO; DERWEN T	OR	ON	2006/02/10 13:47
S66	10268	S65 and S57	US-PGPU B; USPAT; EPO; JPO; DERWEN T	OR	ON	2006/02/10 13:47
S64	5	S57 and S63	US-PGPU B; USPAT; EPO; JPO; DERWEN T	OR	ON	2006/02/10 13:46
S63	231	S55 and S62	US-PGPU B; USPAT; EPO; JPO; DERWEN T	OR	ON	2006/02/10 13:13
S62	371	(analog near2 digital near2 (convertor or converter) or DAC or ADC) near4 delay near2 tim\$3	US-PGPU B; USPAT; EPO; JPO; DERWEN T	OR	ON	2006/02/10 13:13
S59	5	(analog near2 digital near2 (convertor or converter) or DAC or ADC) near4 (adjust or delay) near2 tim\$3 with sampling adj rate	US-PGPU B; USPAT; EPO; JPO; DERWEN T	OR	ON	2006/02/10 13:12
S60	2	S59 and S55	US-PGPU B; USPAT; EPO; JPO; DERWEN T	OR	ON	2006/02/10 13:11

S53	1221	(analog near2 digital near2 (convertor or converter) or DAC or ADC) near5 sampling adj rate	US-PGPU B; USPAT; EPO; JPO; DERWEN T	OR	ON	2006/02/10 13:10
S58	6	S56 and S57	US-PGPU B; USPAT; EPO; JPO; DERWEN T	OR	ON	2006/02/10 13:08
S57	23929	antenna near array	US-PGPU B; USPAT; EPO; JPO; DERWEN T	OR	ON	2006/02/10 13:08
S56	617	S53 and S55	US-PGPU B; USPAT; EPO; JPO; DERWEN T	OR	ON	2006/02/10 13:07
S55	18910530	@ad<"20000303"	US-PGPU B; USPAT; EPO; JPO; DERWEN T	OR	ON	2006/02/10 13:07
S54	892	(analog near2 digital near2 (convertor or converter) or DAC or ADC) near3 sampling adj rate	US-PGPU B; USPAT; EPO; JPO; DERWEN T	OR	ON	2006/02/10 13:07
S52	0	S46 and S51	US-PGPU B; USPAT; EPO; JPO; DERWEN T	OR	ON	2006/02/10 13:05
S51	337034	antenna	US-PGPU B; USPAT; EPO; JPO; DERWEN T	OR	ON	2006/02/10 11:41
S50	1	S46 and S49	US-PGPU B; USPAT; EPO; JPO; DERWEN T	OR	ON	2006/02/10 11:41

S49	825406	array	US-PGPU B; USPAT; EPO; JPO; DERWEN T	OR	ON	2006/02/10 11:41
S48	0	S46 and S47	US-PGPU B; USPAT; EPO; JPO; DERWEN T	OR	ON	2006/02/10 11:40
S47	25511	antenna near2 array	US-PGPU B; USPAT; EPO; JPO; DERWEN T	OR	ON	2006/02/10 11:40
S46	8	S44 and S45	US-PGPU B; USPAT; EPO; JPO; DERWEN T	OR	ON	2006/02/10 11:40
S45	1891053 0	@ad<"20000303"	US-PGPU B; USPAT; EPO; JPO; DERWEN T	OR	ON	2006/02/10 11:39
S44	13	phase adj shifter near2 delay\$3 near4 synchroni\$5	US-PGPU B; USPAT; EPO; JPO; DERWEN T	OR	ON	2006/02/10 11:39
S43	1436	phase adj shifter near2 delay\$3	US-PGPU B; USPAT; EPO; JPO; DERWEN T	OR	ON	2006/02/10 11:38
S42	6796	phase adj shift\$3 near3 delay\$3	US-PGPU B; USPAT; EPO; JPO; DERWEN T	OR	ON	2006/02/10 11:38
S41	1	("5084708").PN.	US-PGPU B; USPAT	OR	OFF	2006/02/10 11:37
S40	1	("20020013133").PN.	US-PGPU B; USPAT	OR	OFF	2006/02/10 11:31

S1	0	"455"/\$.ccls. and phase\$2 near array same tranceiver and adc and dac and programmable near2 delay\$3	US-PGPU B; USPAT	OR	ON	2006/02/09 18:08
S39	1	("20020013133").PN.	US-PGPU B; USPAT	OR	OFF	2005/05/23 13:49
S38	31	713/200.ccls. and firewall.ti.	US-PGPU B; USPAT	OR	ON	2005/05/23 13:48
S37	4	713/200,201,202.ccls. and maintain near3 firewall.ab.	US-PGPU B; USPAT	OR	ON	2005/05/23 13:01
S36	0	713/200,201,202.ccls. and maintain near3 firewall.ti.	US-PGPU B; USPAT	OR	ON	2005/05/23 12:59
S35	159	713/200,201,202.ccls. and firewall.ti	US-PGPU B; USPAT	OR	ON	2005/05/23 12:58
S34	8	antenna near4 beam with electronic\$5 near2 control\$3 and (dac or adc or digital near3 analog near3 converter)	US-PGPU B; USPAT	OR	ON	2005/05/23 12:54
S33	1	("5907517").PN.	US-PGPU B; USPAT, USOCR	OR	OFF	2004/09/14 17:58
S32	4	@ad<="20000303" and program\$5 adj2 fet with delay	US-PGPU B; USPAT	OR	ON	2004/09/14 17:03
S31	0	@ad<="20000303" and programmable adj2 fet with delay	US-PGPU B; USPAT	OR	ON	2004/09/14 17:03
S30	0	@ad<="20000303" and programmable adj2 fet near4 delay	US-PGPU B; USPAT	OR	ON	2004/09/14 17:02
S29	12	@ad<="20000303" and programmable adj fet	US-PGPU B; USPAT	OR	ON	2004/09/14 17:02
S28	38	@ad<="20000303" and program\$5 adj fet	US-PGPU B; USPAT	OR	ON	2004/09/14 17:01
S27	148	@ad<="20000303" and program\$5 adj3 fet	US-PGPU B; USPAT	OR	ON	2004/09/14 17:01
S26	5	@ad<="20000303" and program\$5 same diode adj phase adj shifter	US-PGPU B; USPAT	OR	ON	2004/09/14 17:00
S25	0	@ad<="20000303" and program\$5 near9 diode adj phase adj shifter	US-PGPU B; USPAT	OR	ON	2004/09/14 16:54

S24	0	@ad<="20000303" and program\$5 near3 diode adj phase adj shifter	US-PGPU B; USPAT	OR	ON	2004/09/14 16:53
S23	0	@ad<="20000303" and programmable near3 diode adj phase adj shifter	US-PGPU B; USPAT	OR	ON	2004/09/14 16:53
S22	0	@ad<="20000303" and programmable adj diode adj phase adj shifter	US-PGPU B; USPAT	OR	ON	2004/09/14 16:53
S21	214	@ad<="20000303" and diode adj phase adj shifter	US-PGPU B; USPAT	OR	ON	2004/09/14 16:53
S20	16	MEMS same phase adj shifters and @ad<="20000303"	US-PGPU B; USPAT	OR	ON	2004/09/14 16:52
S19	105	MEMS same phase adj shifters	US-PGPU B; USPAT	OR	ON	2004/09/14 16:18
S18	7	"455"/\$.ccls. and MEMS and phase adj shifters	US-PGPU B; USPAT	OR	ON	2004/09/14 16:18
S17	2	"455"/\$.ccls. and MEMS same phase adj shifters	US-PGPU B; USPAT	OR	ON	2004/09/14 16:17
S16	0	"455"/\$.ccls. and MEMS with phase adj shifters	US-PGPU B; USPAT	OR	ON	2004/09/14 16:14
S15	0	"455"/\$.ccls. and MEMS near3 phase adj shifters	US-PGPU B; USPAT	OR	ON	2004/09/14 16:14
S14	0	"455"/\$.ccls. and MEMS near phase adj shifters	US-PGPU B; USPAT	OR	ON	2004/09/14 16:14
S13	249	"455"/\$.ccls. and MEMS	US-PGPU B; USPAT	OR	ON	2004/09/14 16:07
S12	10	"455"/\$.ccls. and phase\$2 near2 array and program\$5 near4 delay and clock	US-PGPU B; USPAT	OR	ON	2004/09/14 16:06
S11	2	"455"/\$.ccls. and phase\$2 near2 array and programmable near2 delay and clock	US-PGPU B; USPAT	OR	ON	2004/09/10 16:03
S10	136	"455"/\$.ccls. and phase\$2 near2 array and delay and clock	US-PGPU B; USPAT	OR	ON	2004/09/10 16:03
S9	8	"455"/\$.ccls. and phase\$2 near2 array and adc and dac and delay and clock	US-PGPU B; USPAT	OR	ON	2004/09/10 16:03

S8	13	"455"/\$.ccls. and phase\$2 near2 array and transceiver and adc and dac	US-PGPU B; USPAT	OR	ON	2004/09/10 15:45
S7	5	"455"/\$.ccls. and phase\$2 near array and transceiver and adc and dac and delay\$3	US-PGPU B; USPAT	OR	ON	2004/09/10 15:34
S6	0	"455"/\$.ccls. and phase\$2 near array and transceiver and adc and dac and programmable near3 delay\$3	US-PGPU B; USPAT	OR	ON	2004/09/10 15:32
S5	0	"455"/\$.ccls. and phase\$2 near3 array and traneiver and adc and dac and delay\$3	US-PGPU B; USPAT	OR	ON	2004/09/10 15:31
S4	0	"455"/\$.ccls. and phase\$2 near array and traneiver and adc and dac and delay\$3	US-PGPU B; USPAT	OR	ON	2004/09/10 15:30
S3	0	"455"/\$.ccls. and phase\$2 near array and traneiver and adc and dac and programmable with delay\$3	US-PGPU B; USPAT	OR	ON	2004/09/10 15:30
S2	0	"455"/\$.ccls. and phase\$2 near array same traneiver and adc and dac and programmable with delay\$3	US-PGPU B; USPAT	OR	ON	2004/09/10 15:30